JUL 0 5 2005	2A 301
INFORM	TION DISCLOSURE STATEMENT

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	Attorney Docket Number	1011-67627-01
	Application Number	10/781,031
	Filing Date	February 17, 2004
	First Named Inventor	Rajski
	Art Unit	2193
	Examiner Name	David H. Malzahn

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee

FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee
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		Japan	03-002579	1.8.1991	Shuichi et al.
		Japan	05-215816	8.27.1993	Yasushi
		- Japan	11-030646	2.2.1999	Hideki)
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Examiner's Initials*	Cite No. (optional)					
		Lew Yan Voon et al., "BIST Linear Generator based on Complemented Outputs," IEEE J VLSI Test Symposium, pp. 137-142 (1992).				

EXAMINER SIGNATURE: DATE CONSIDERED: 9/27/05

^{*} Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.